

FIG.1

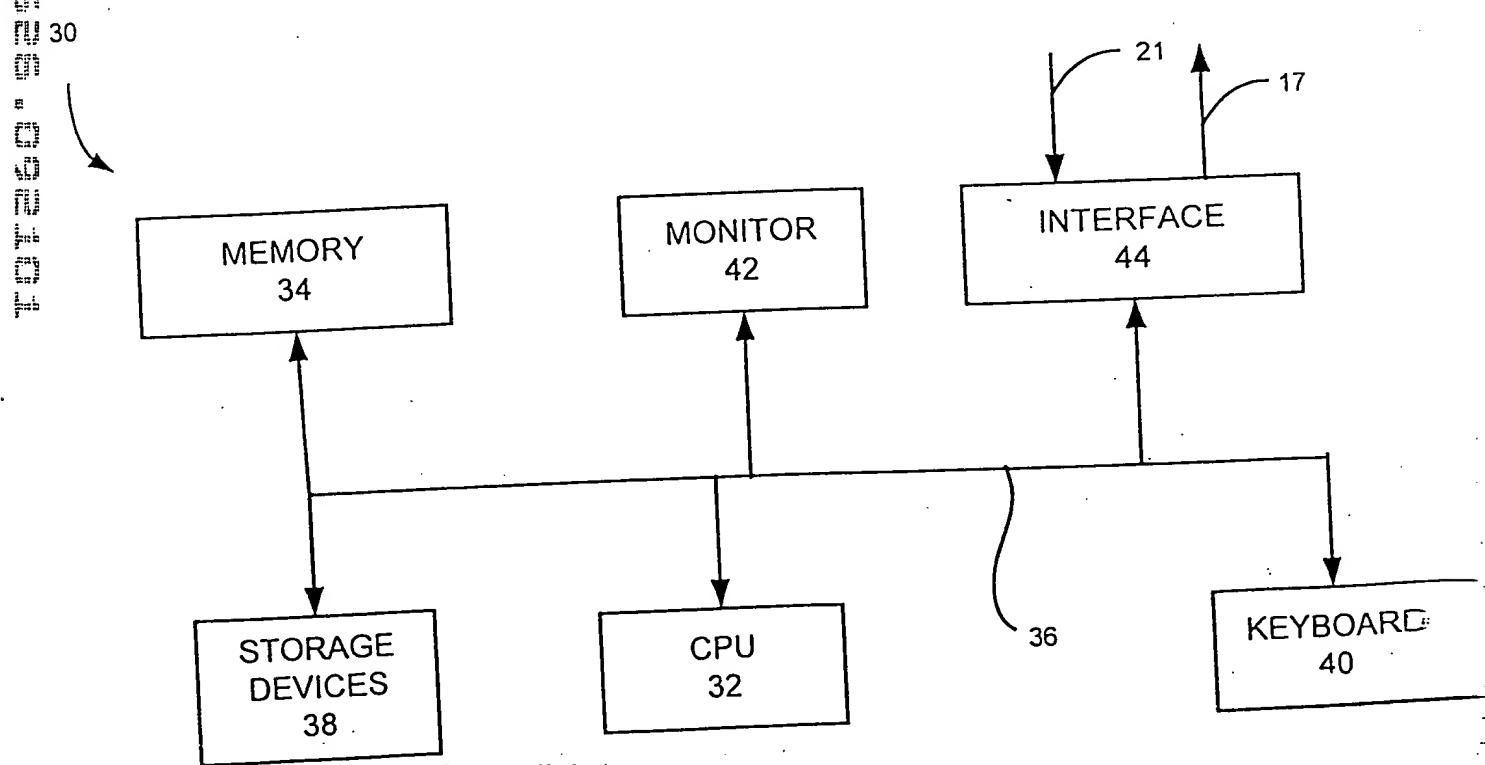


FIG.2

02960662 02960663 02960664

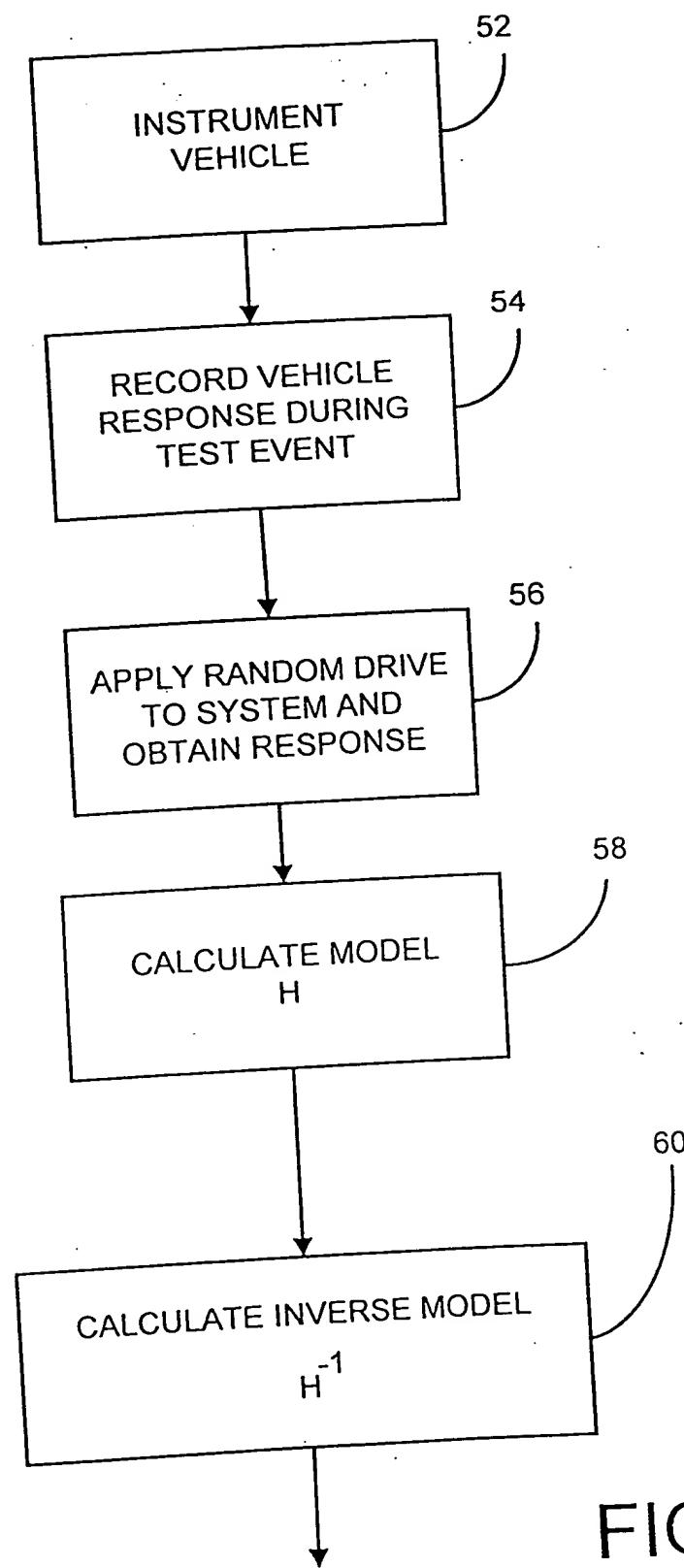
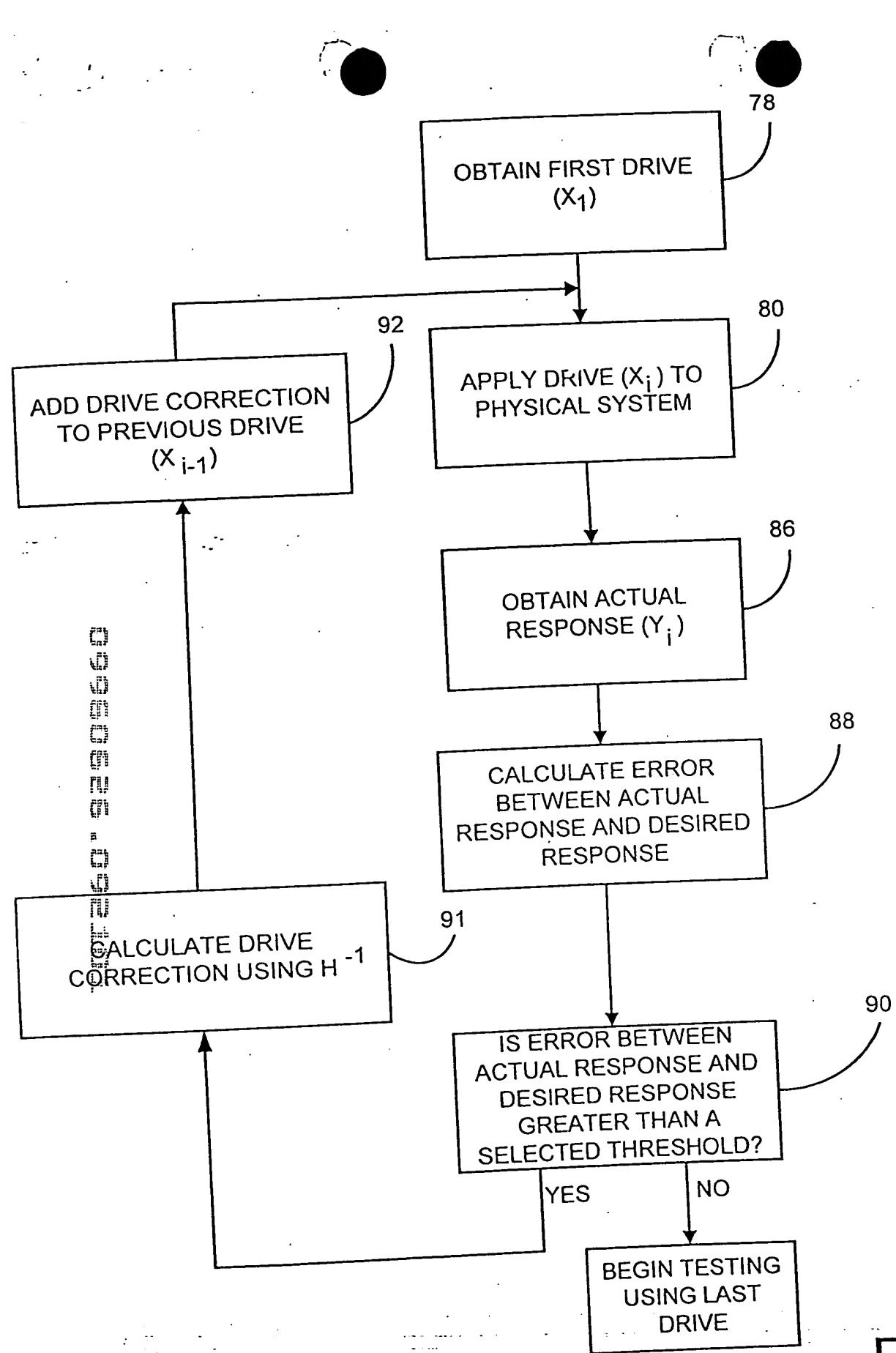


FIG. 3A

FIG. 3B

OR

FIG. 3C



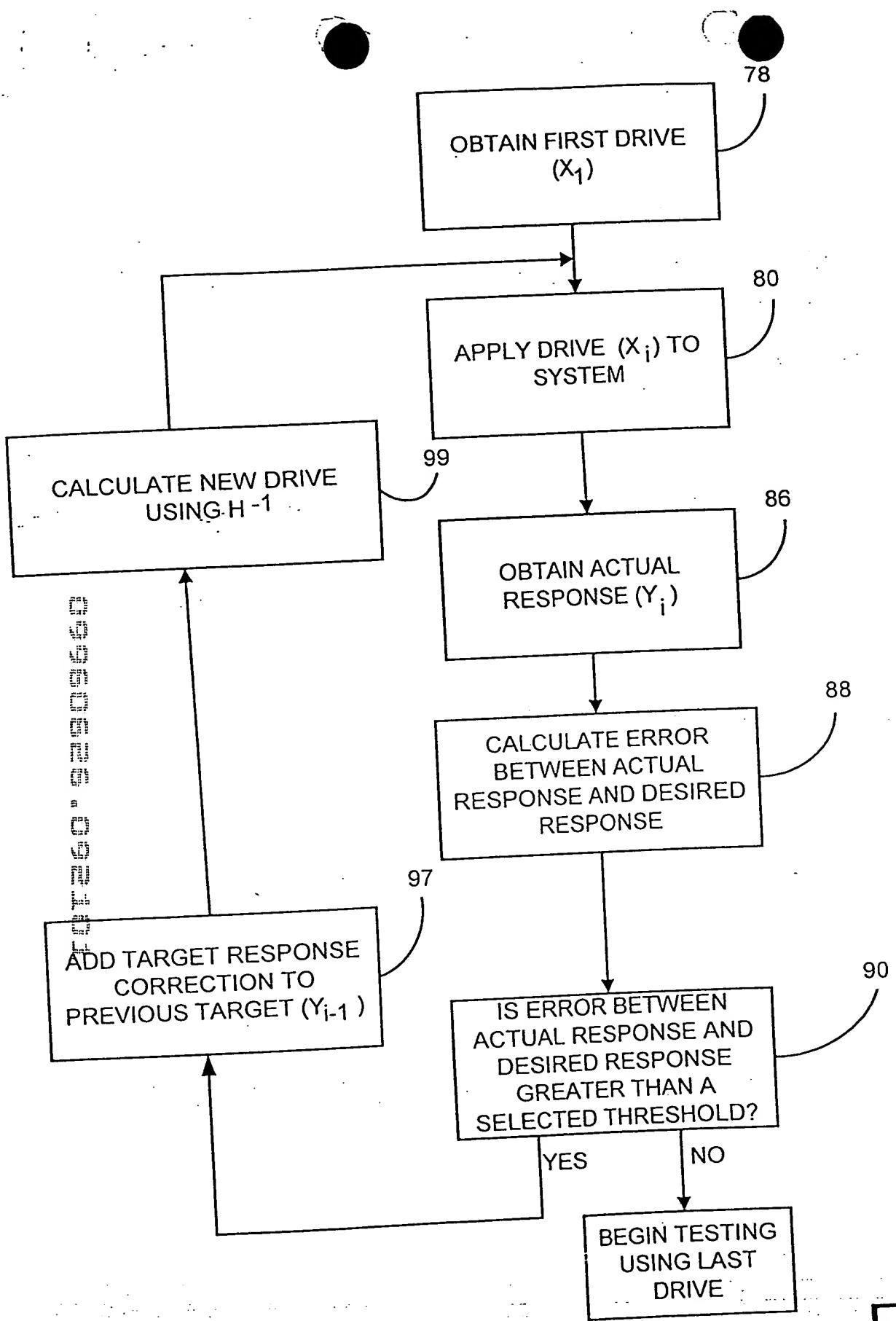


FIG. 3C

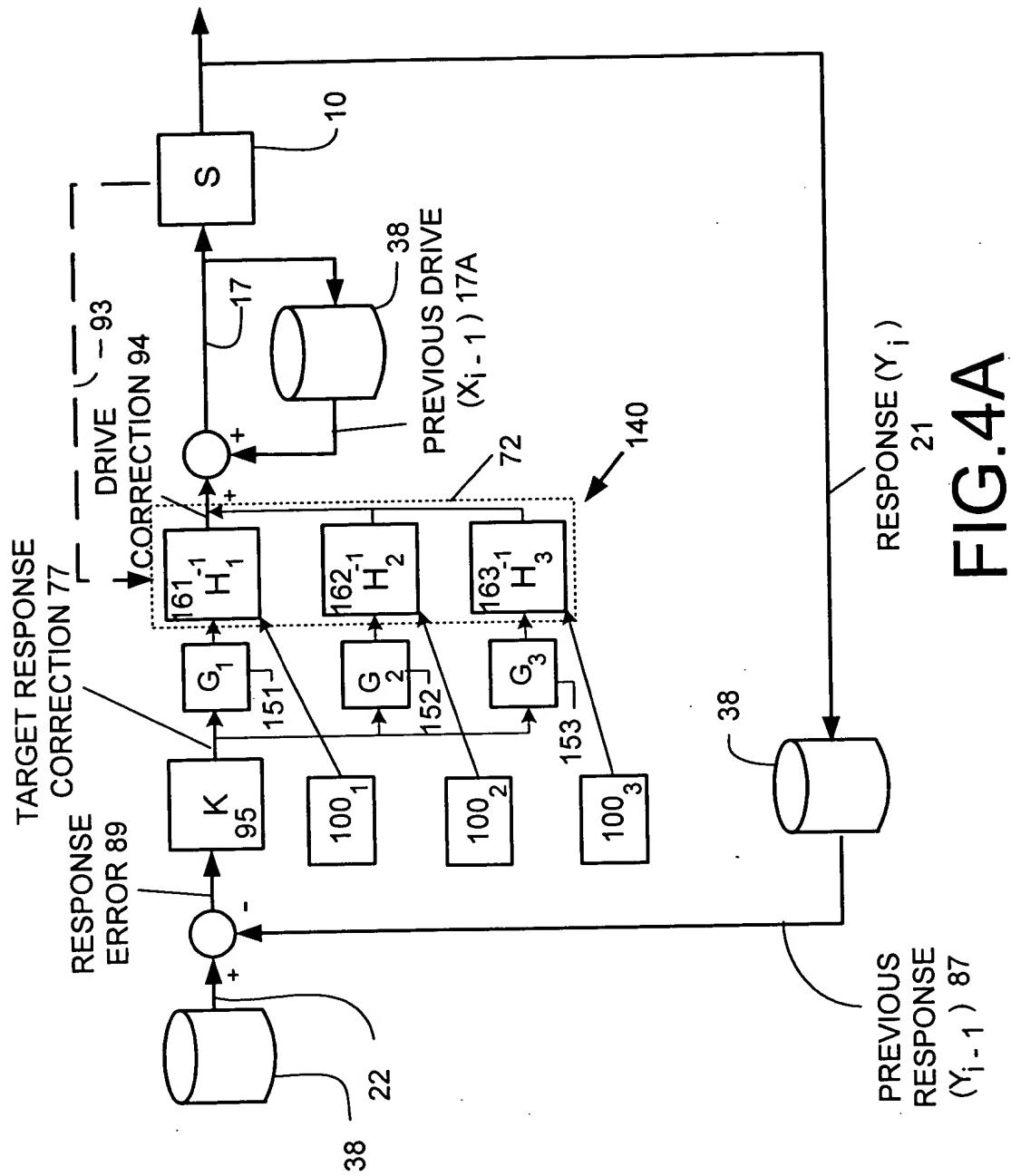


FIG. 4A

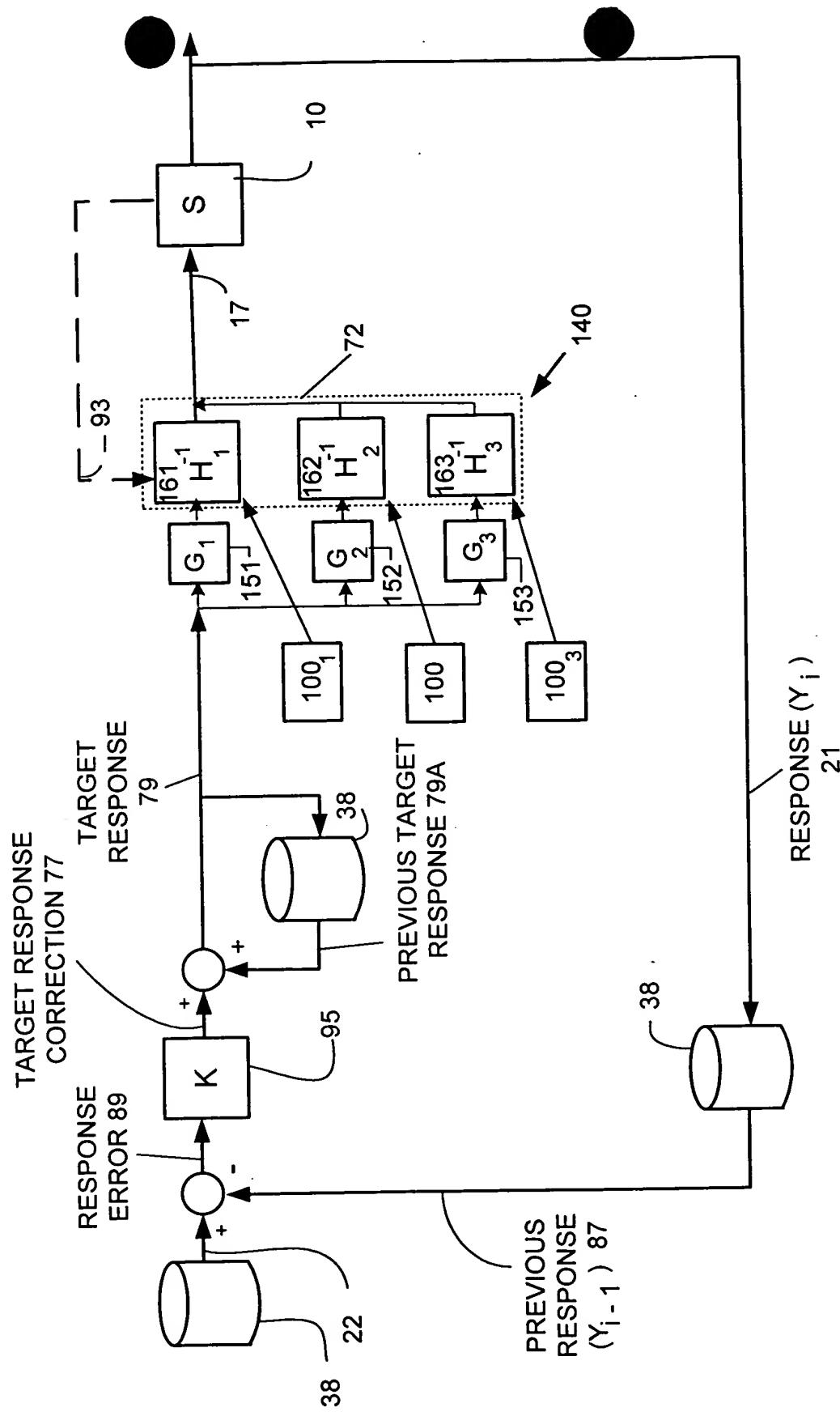


FIG. 4B

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

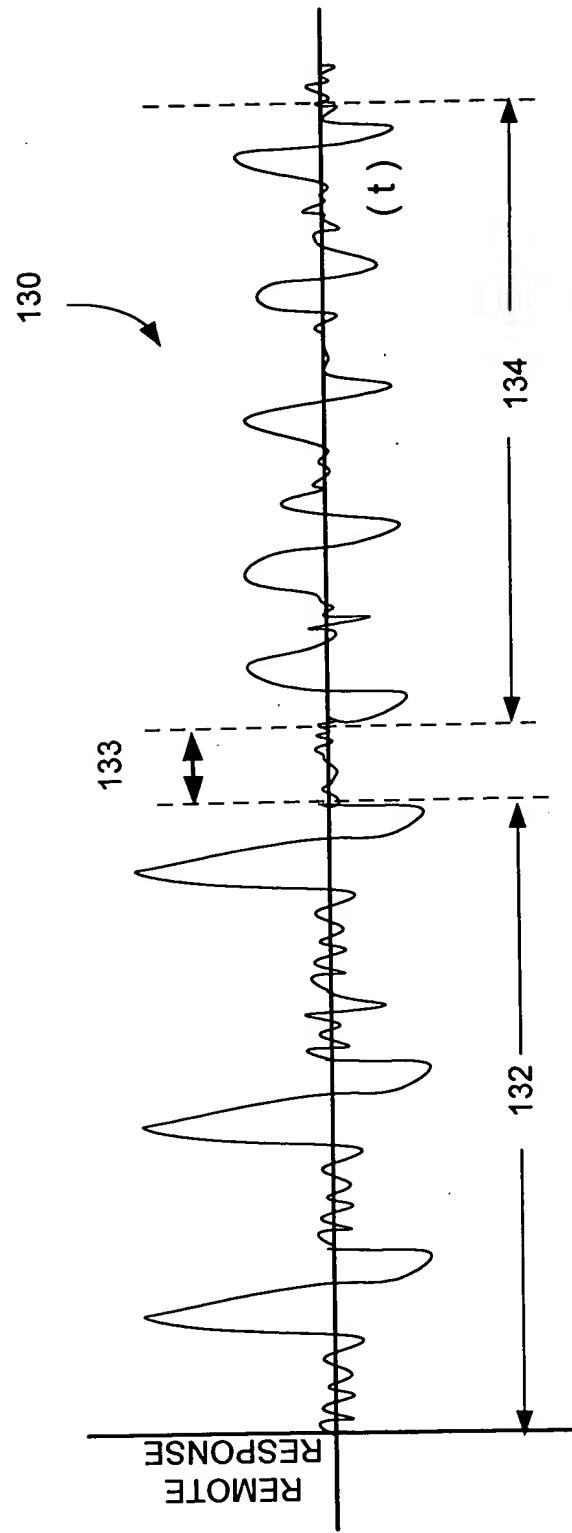


FIG. 5

TOP SECRET

140

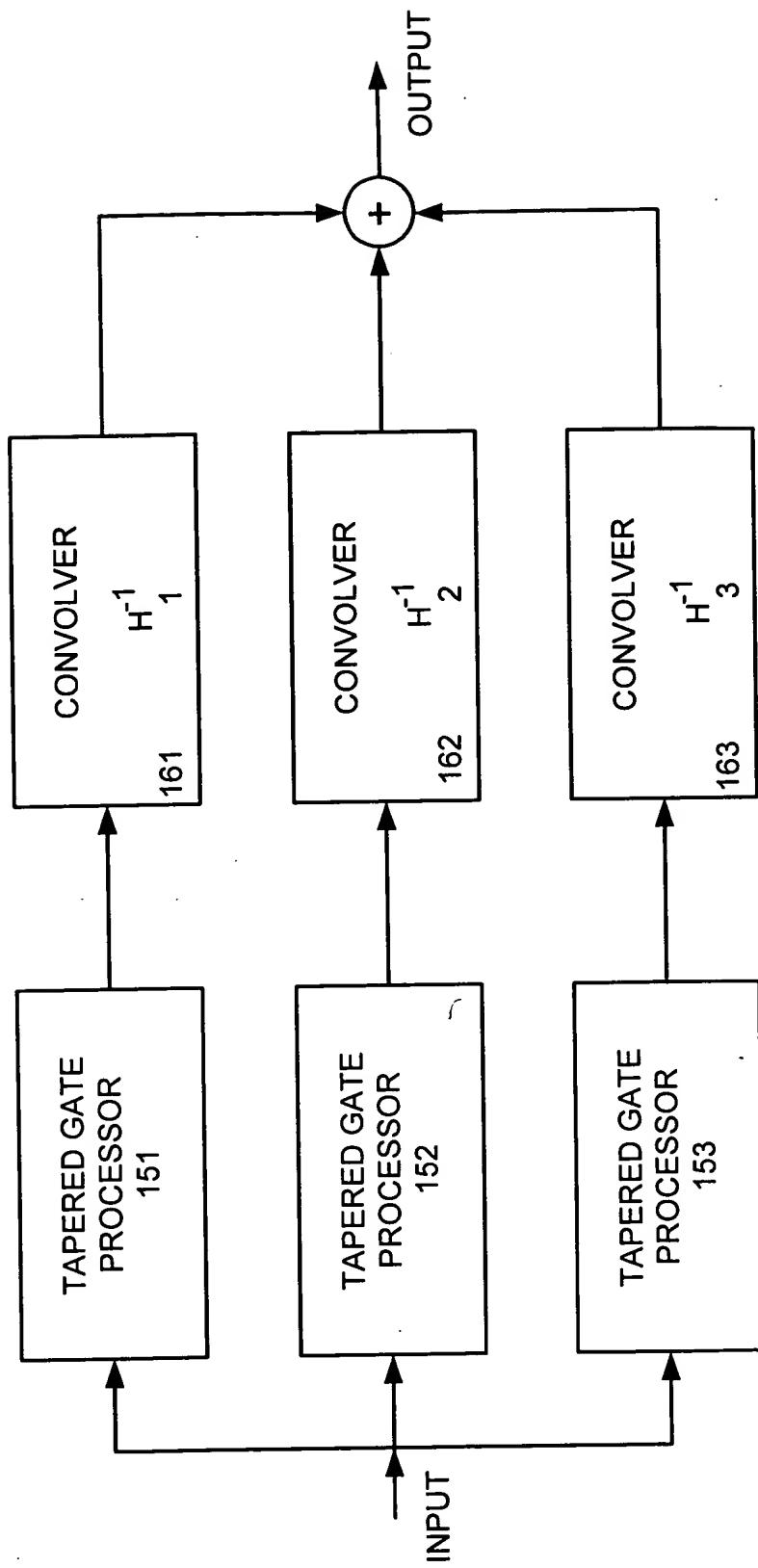
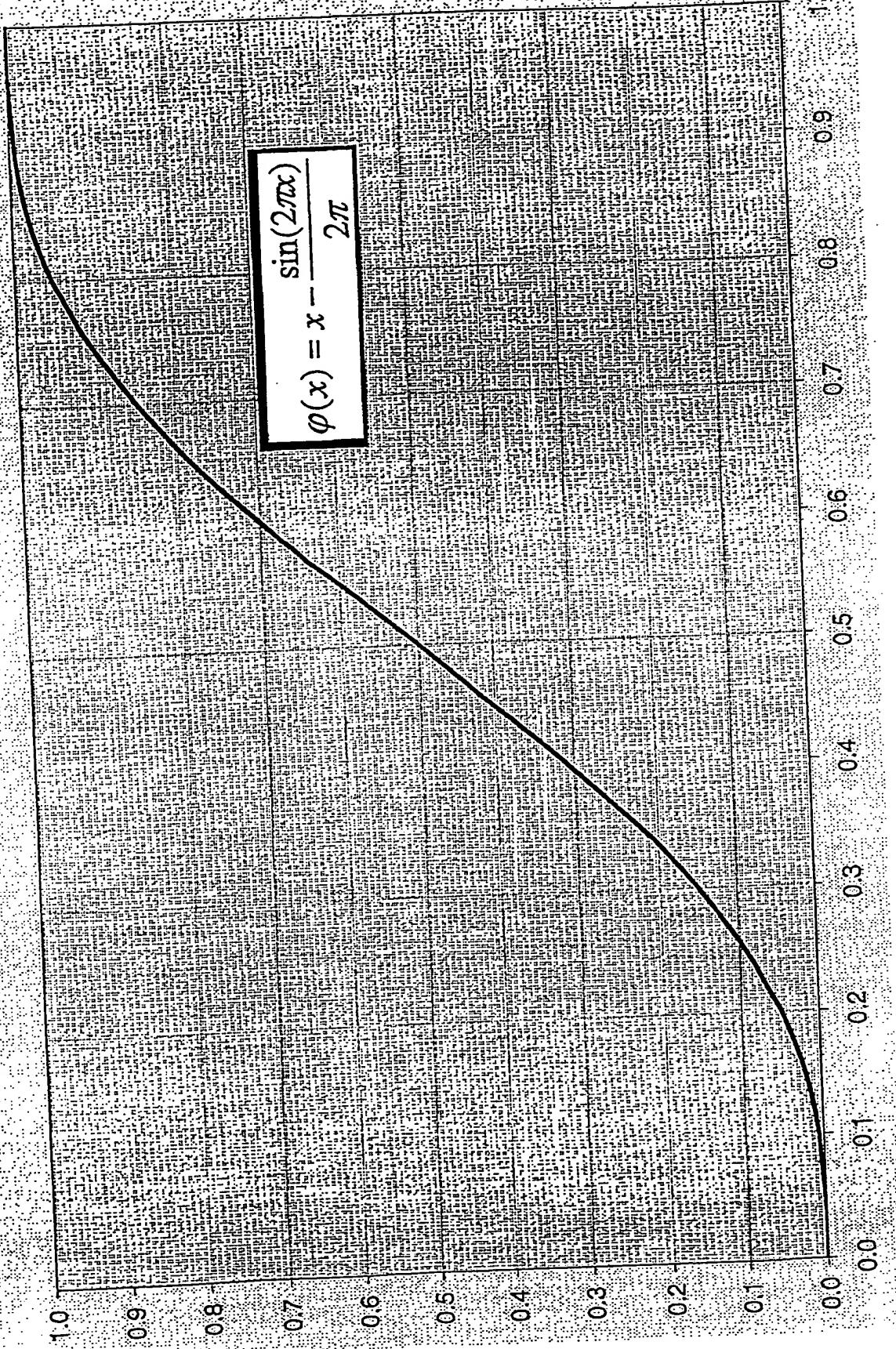


FIG. 6



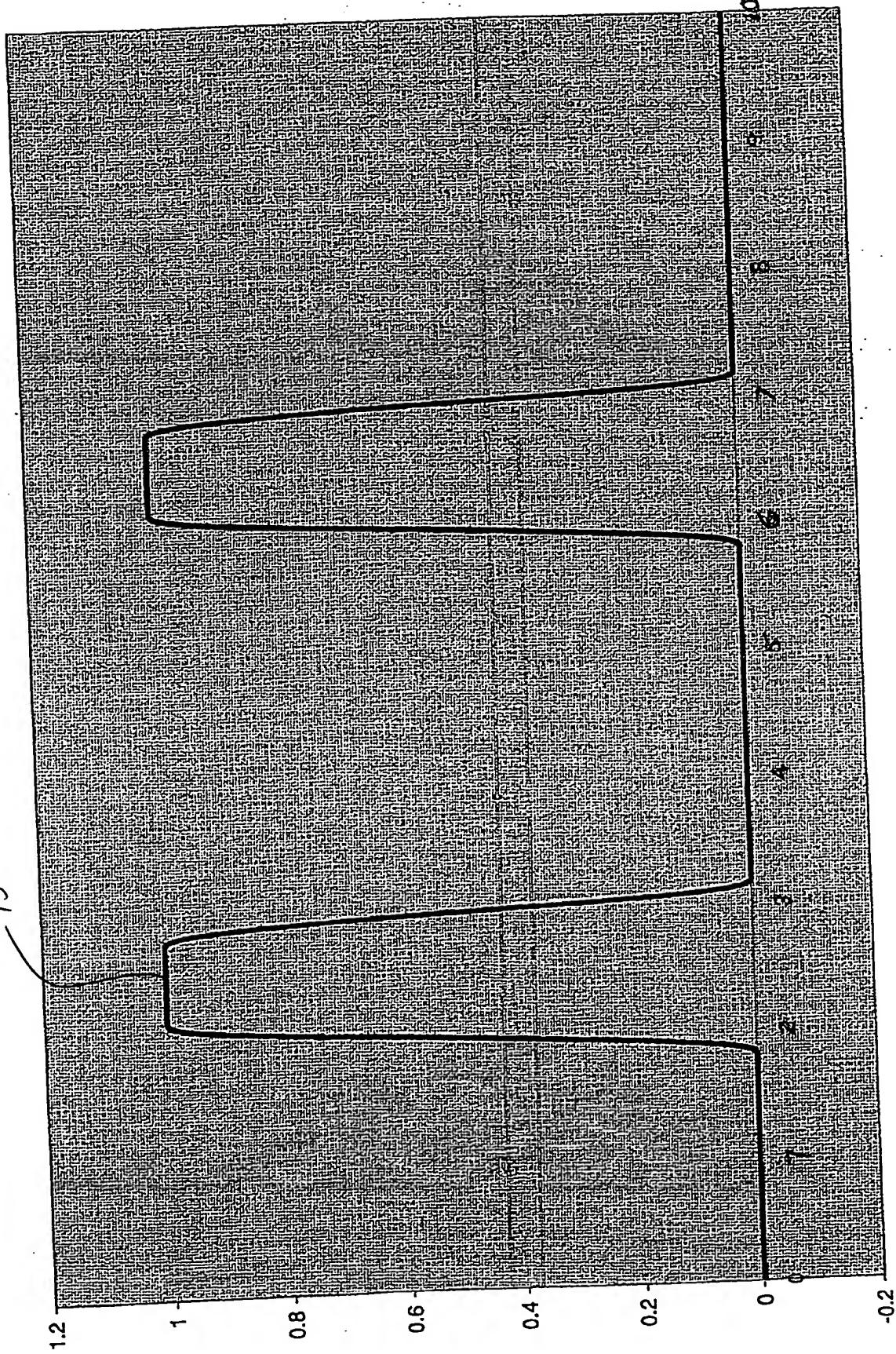
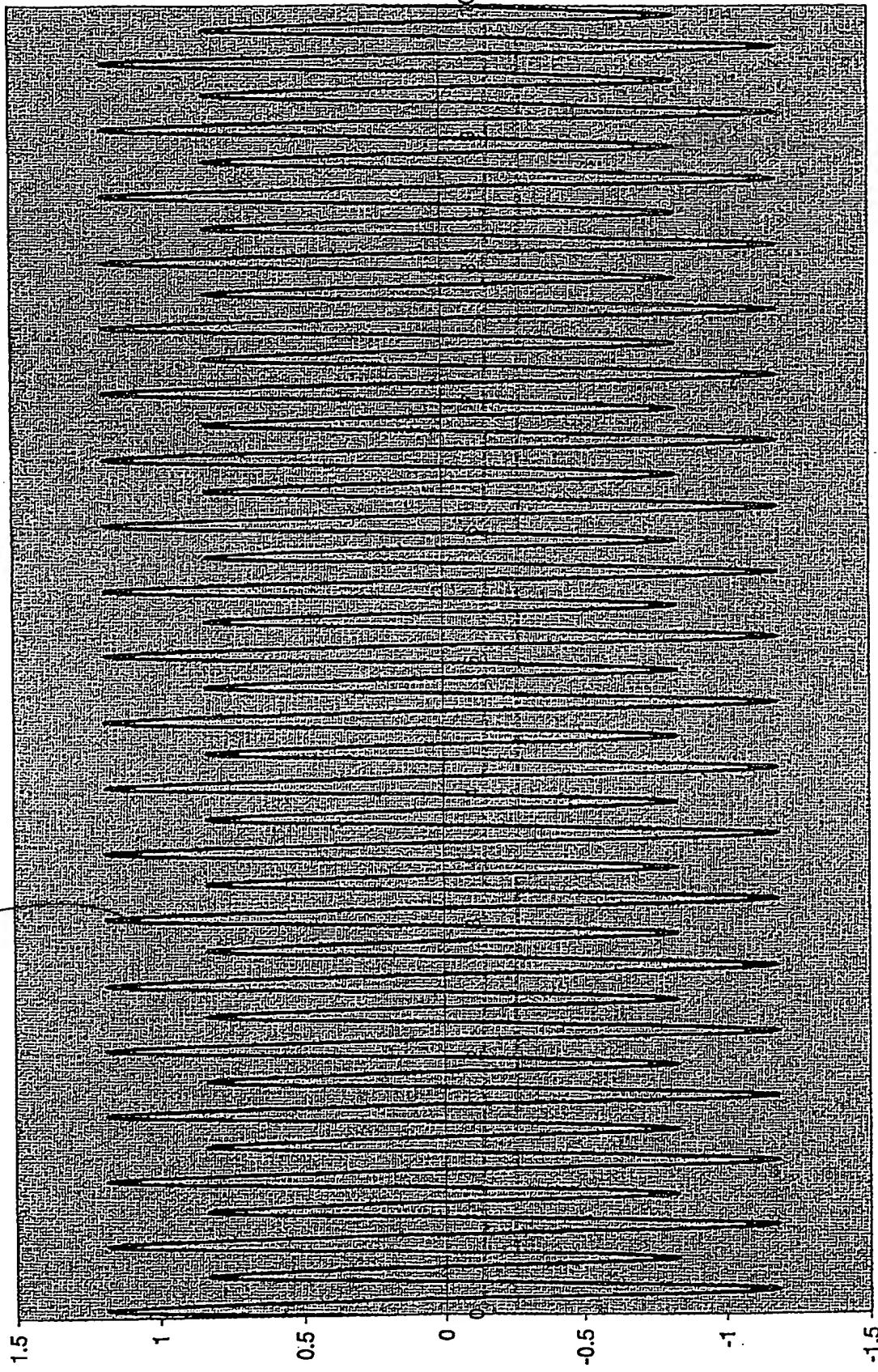


Fig. 8

155

70 70 70 70 70 70 70 70

157



ET 6. 9

0.9350 0.9200 0.8950 0.8600 0.8250

- Gated Input Signal

159

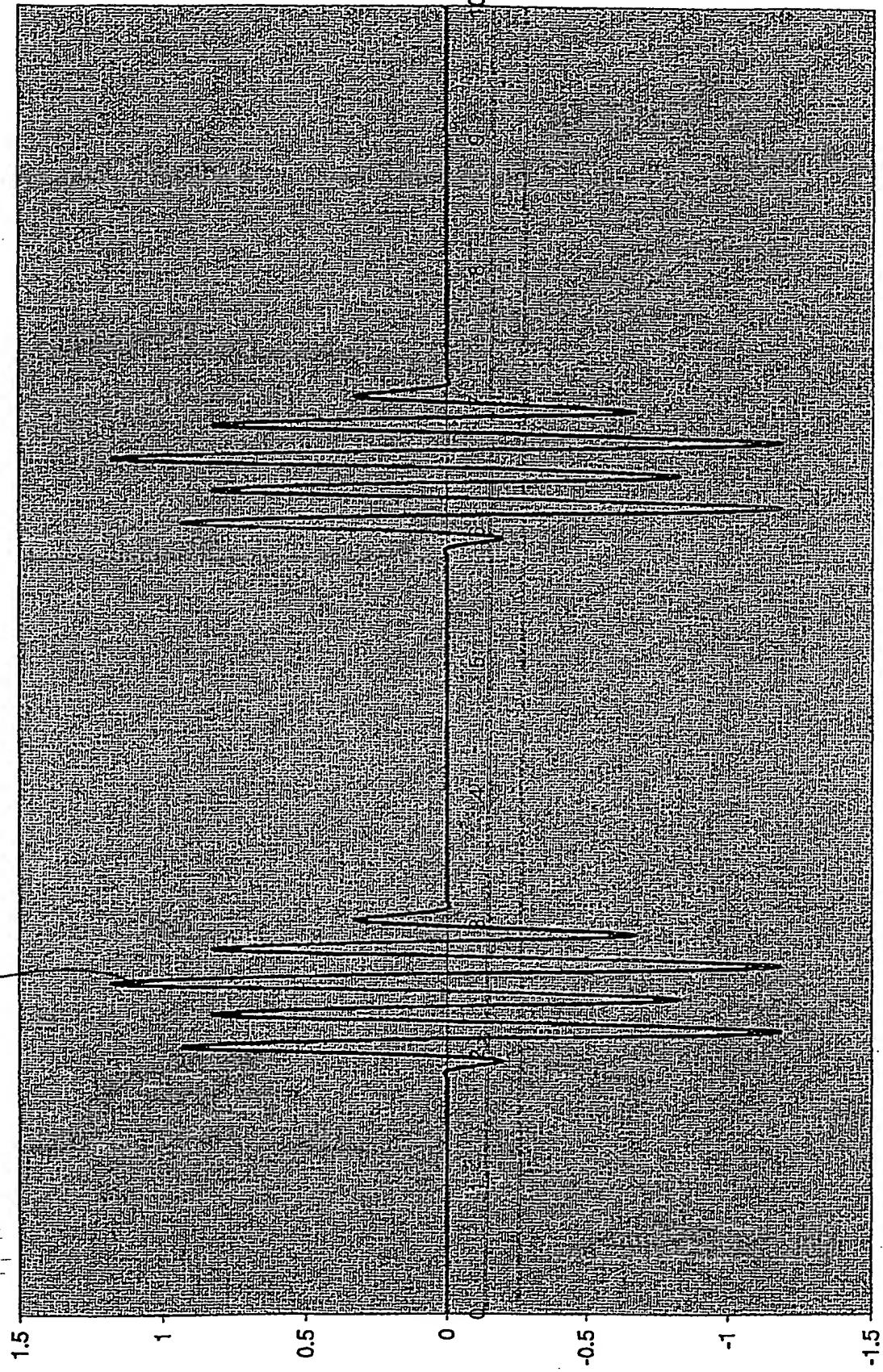


Fig. 10

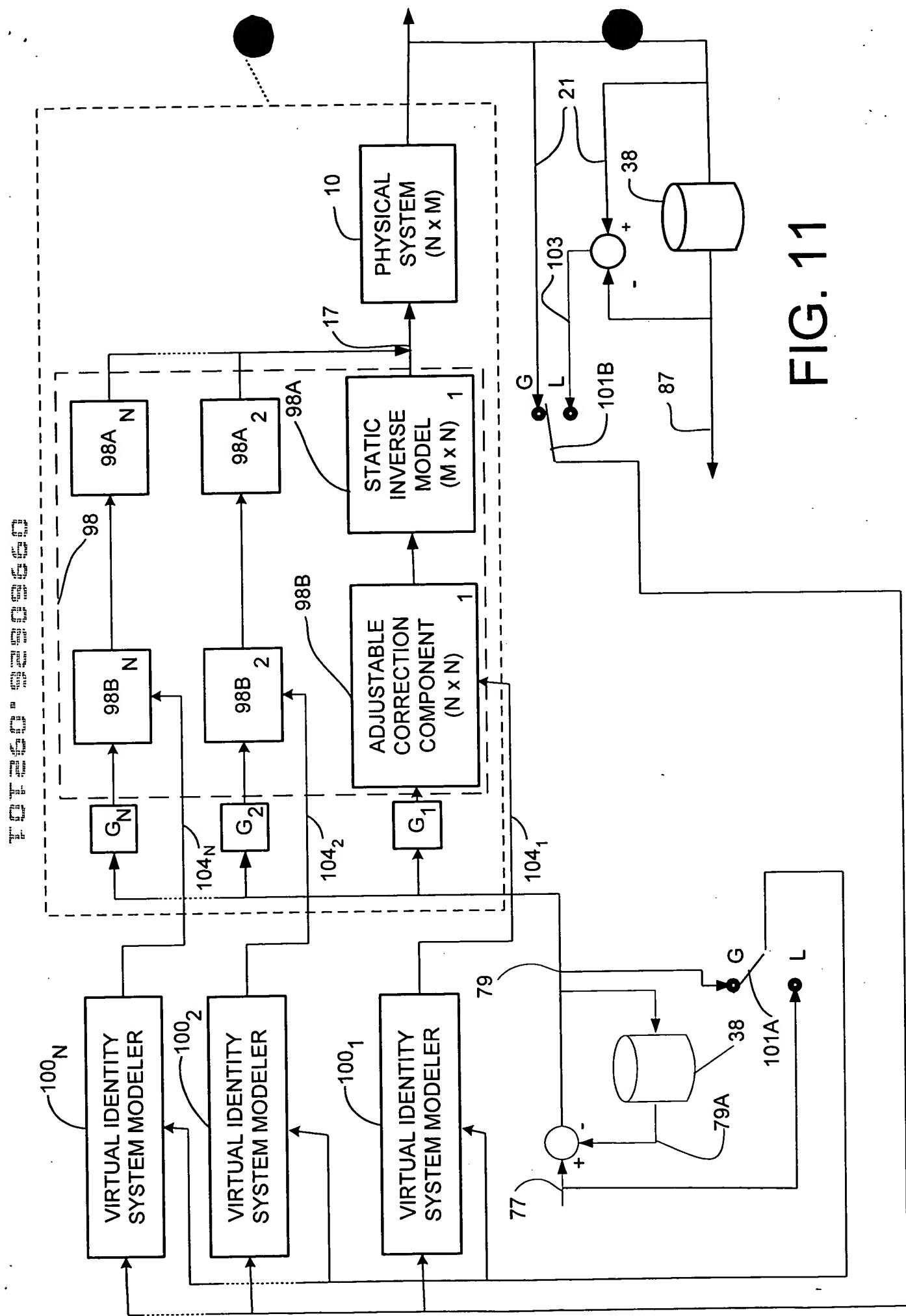


FIG. 1

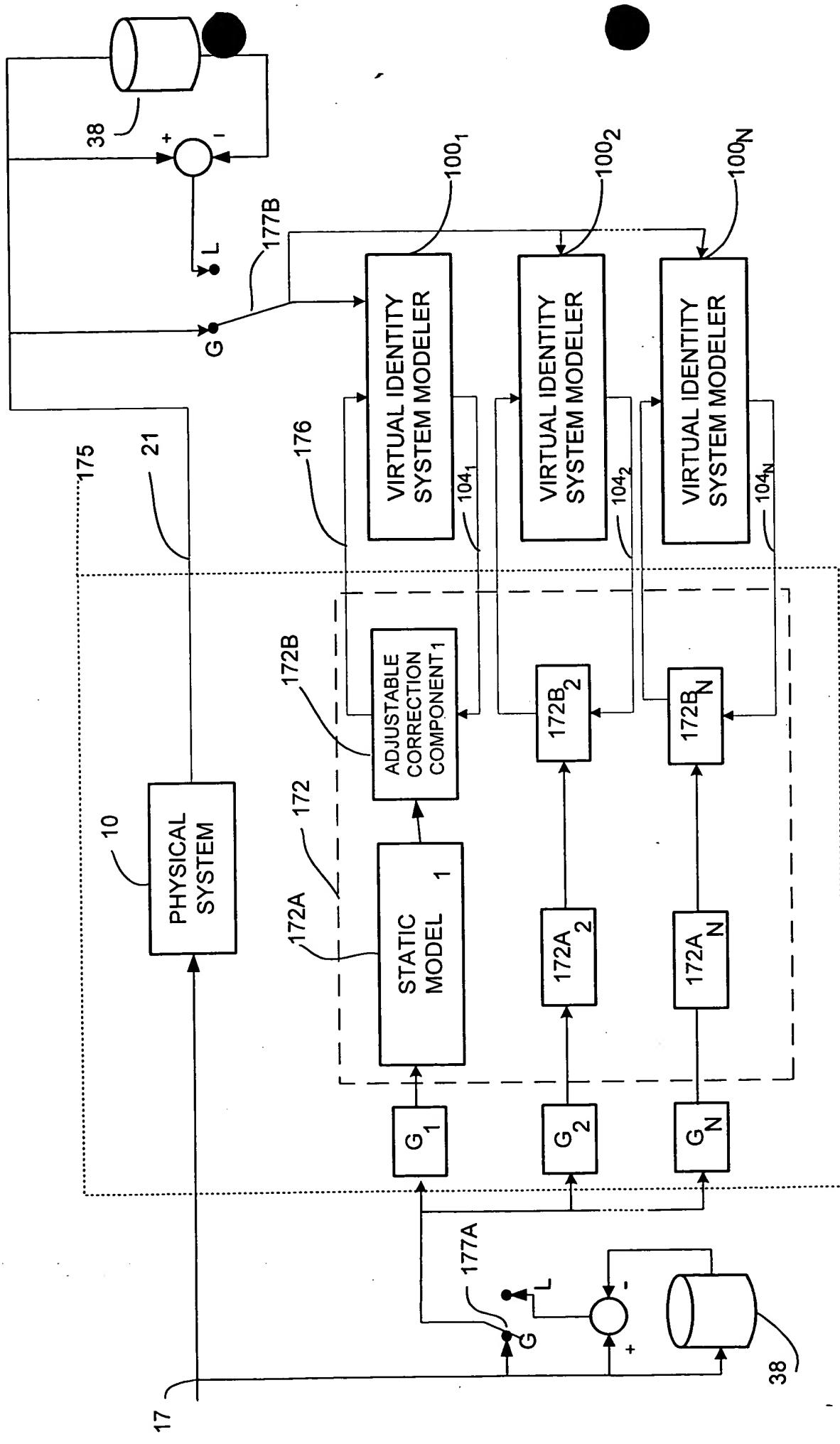


FIG. 12

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